Build a development board for an analog CPLD chip.
Use the Silego SLG46826 to build an analog CPLD development board. The 46826 has an I2C based non-volatile configuration memory that can be reprogrammed over 1000 times. This dev board gives an I2C host interface to that memory and to the internal registers of the SLG46826.

More information on the SLG46826 can be found here:
  https://www.dialog-semiconductor.com/products/slg46826